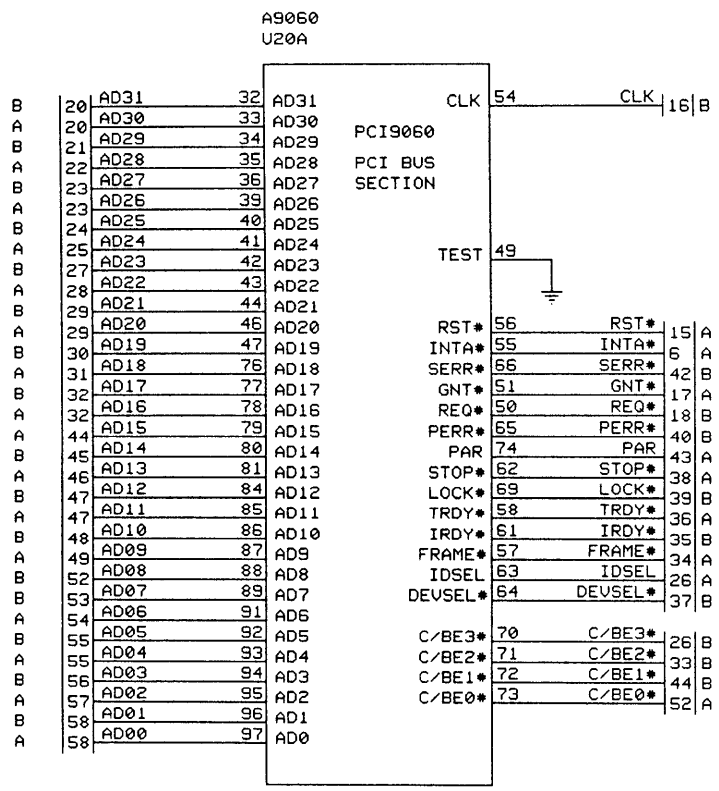


A 4
B 4
TDI - TDO boundary scan not supported

B 11
PRSNT2* @ GND = 15W max

PCI BUS I/O

IKON CORPORATION

PCI DR11 - 1/2 size

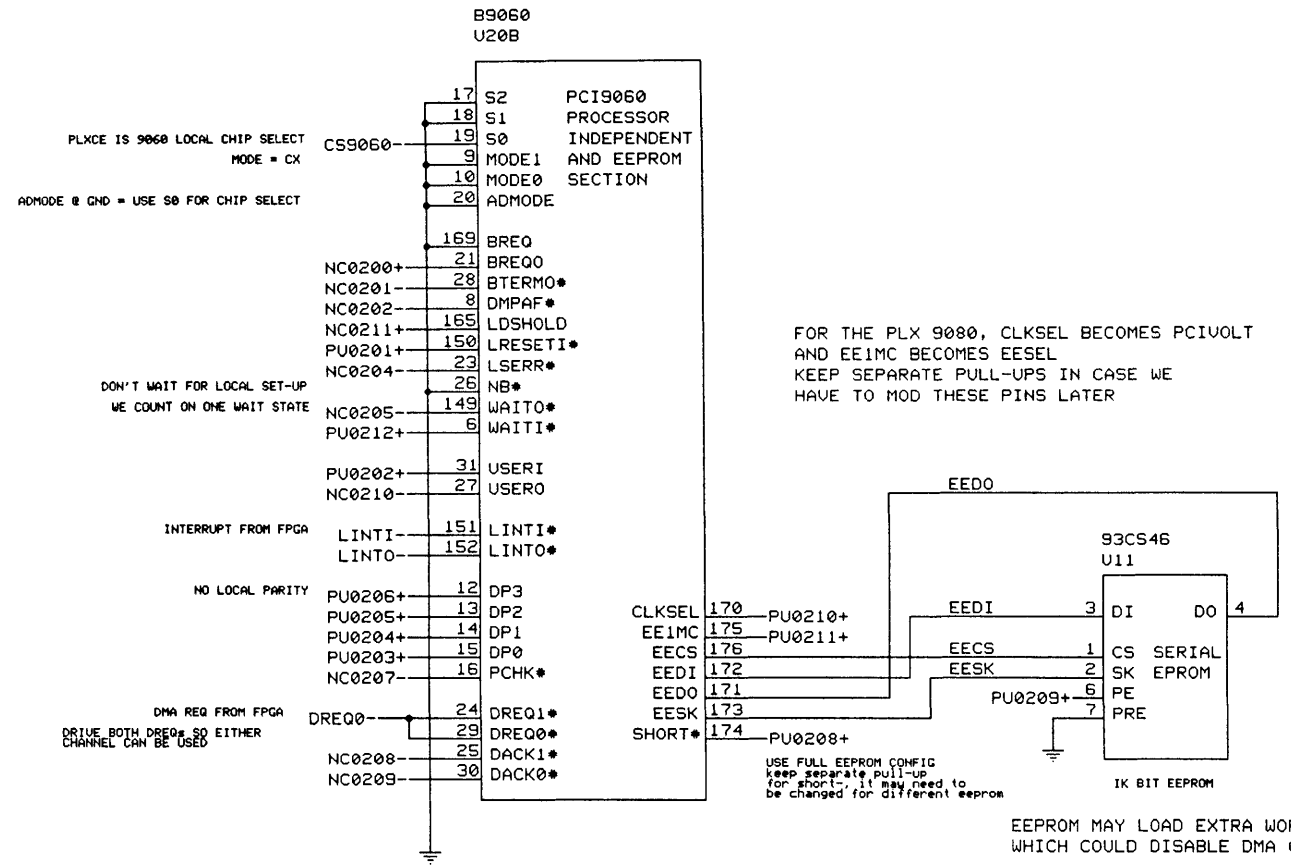
D | 10118 | REV etch

DATE: 12/05/96 13:38:30 | SHEET 01 OF 10

SEPARATE 10KOHM PULL-UPS

NC's SHOULD GO TO VIAS TO NOWHERE!

ALL PINS LABELLED NCxxxx SHOULD
GO TO VIAS FOR POSSIBLE FUTURE CONNECTION



FOR THE PLX 9080, CLKSEL BECOMES PCIVOLT
AND EE1MC BECOMES EESEL
KEEP SEPARATE PULL-UPS IN CASE WE
HAVE TO MOD THESE PINS LATER

EEPROM MAY LOAD EXTRA WORDS INTO DIAG REG
WHICH COULD DISABLE DMA CHANS - SEE DOCS

PROC. IND. LOCAL BUS & EEPROM

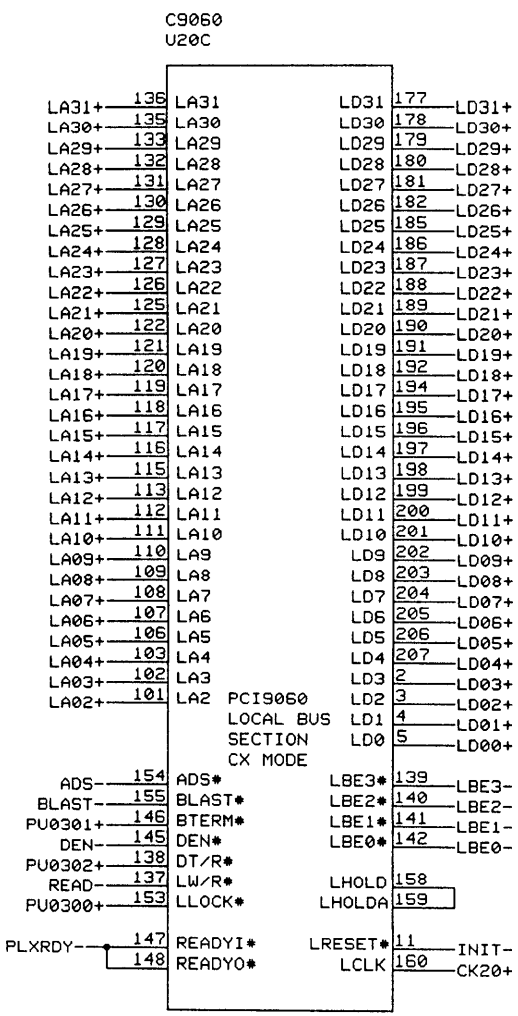
IKON CORPORATION

PCI DR11 - 1/2 size

D | 10118 | REV etch

DATE: 12/05/96 13:38:59 | SHEET 02 OF 10

LA14 -- 31 NOT USED - PULLED UP ONLY
PULL UP ADS, BLAST, DEN, READ
& READY IN/OUT



THESE ALSO HAVE OTHER MEANINGS WHEN IN 16 BIT MODE
- LBE3 = BHE-
- LBE2 = NC
- LBE1 = LA01+
- LBE0 = BLE-

DIRECT SLAVE IO IS 32 BITS
DMA CHAIN READS AND XFERS ARE 16 BITS

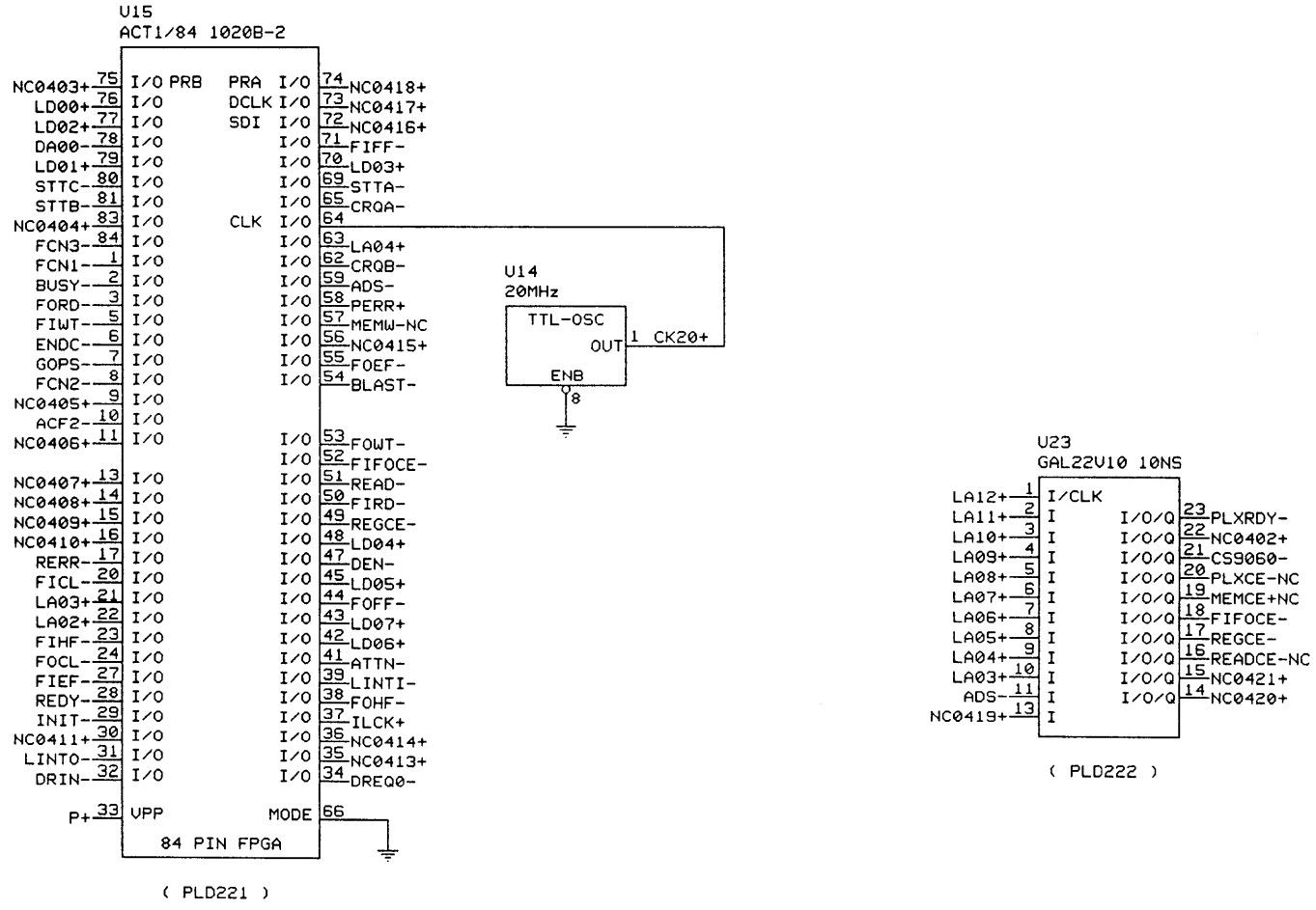
LOCAL BUS

IKON CORPORATION

PCI DR11 - 1/2 size

D		10118	REV etch
DATE: 12/05/96 13:39:45		SHEET 03 OF 10	

4 DECOUPLERS FOR FPGA



FPGA & GALS

IKON CORPORATION

PCI DR11 - 1/2 size

D | 10118 | REV etch

DATE: 12/05/96 13:40:32 | SHEET 04 OF 10

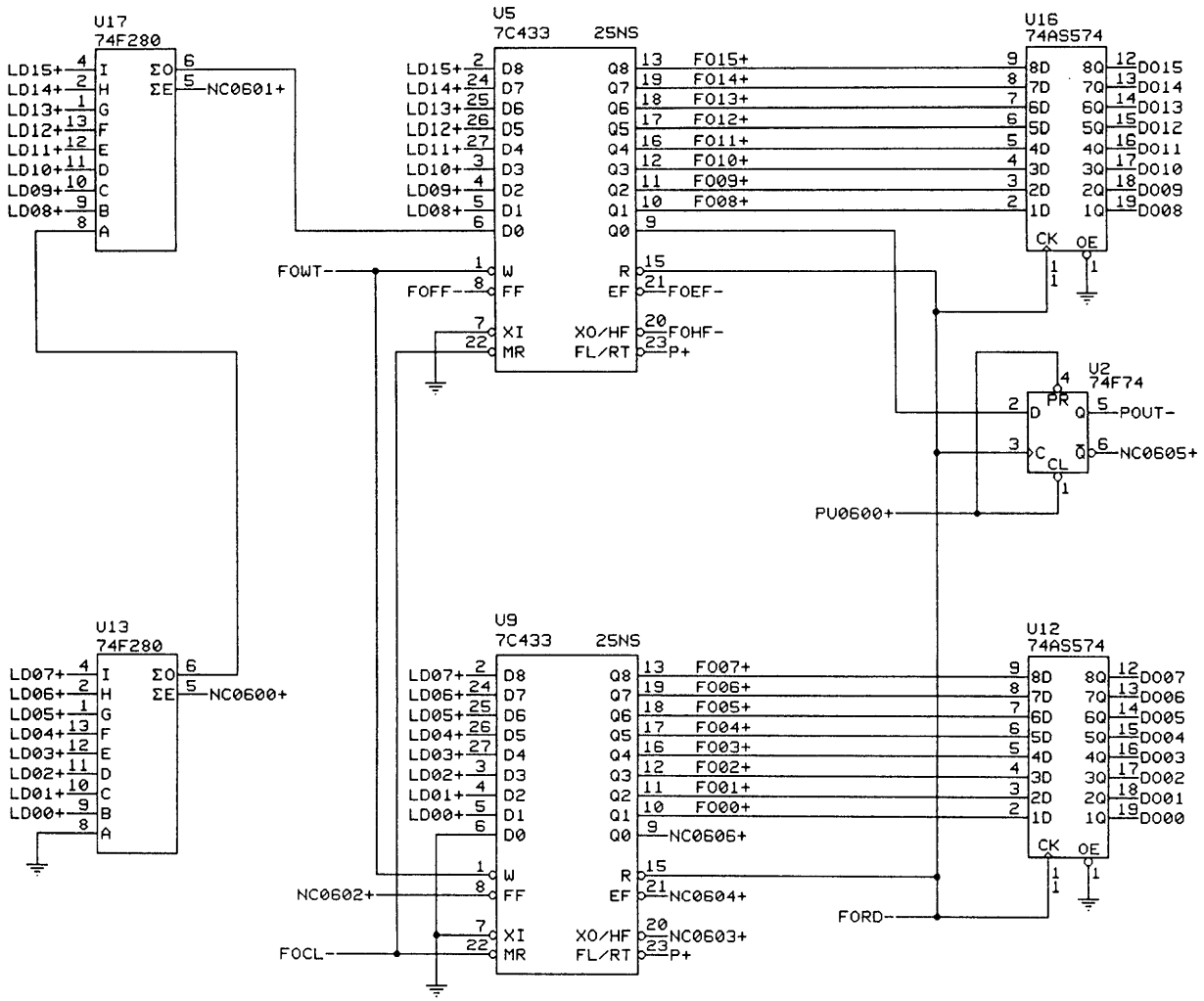
NO LOCAL MEMORY IN 10118

LOCAL MEMORY

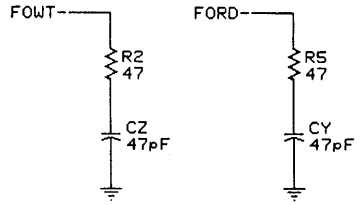
IKON CORPORATION

PCI DR11 - 1/2 size

D		10118	REV etch
DATE: 12/05/96 13:41:18		SHEET 05 OF 10	



FIFO TERMS MUST BE AT END OF TRACE FARTHEST
FROM SIGNAL SOURCE!!!
TRACES MUST BE POINT TO POINT, WITH
NO Ts OR Ys!!!



2 0.300" DECOUPLING CAPS PER FIFO
PLUS ONE SURFACE MOUNT CAP PER FIFO

OUTPUT DATA PATH

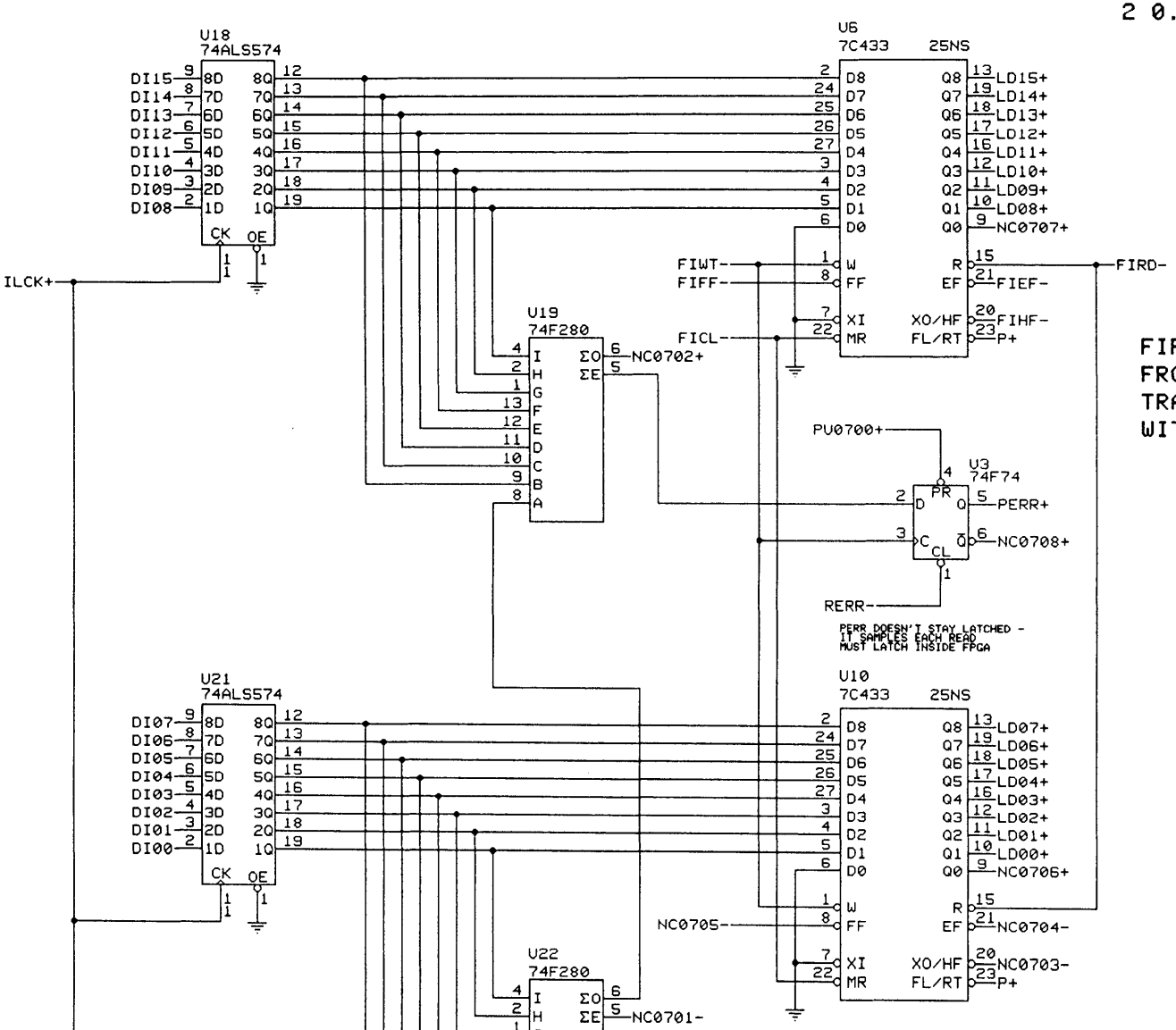
IKON CORPORATION

PCI DR11 - 1/2 size

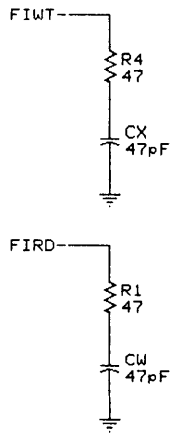
D | MODEL 10118 | REV etch

DATE: 12/05/96 13:42:05 | SHEET 06 OF 10

2 0.300 DECOUPLING CAPS PER FIFO



FIFO TERMS MUST BE AT FAR END OF TRACE
FROM SIGNAL SOURCE !!!
TRACES MUST BE POINT TO POINT,
WITH NO Ts OR Ys!!!!



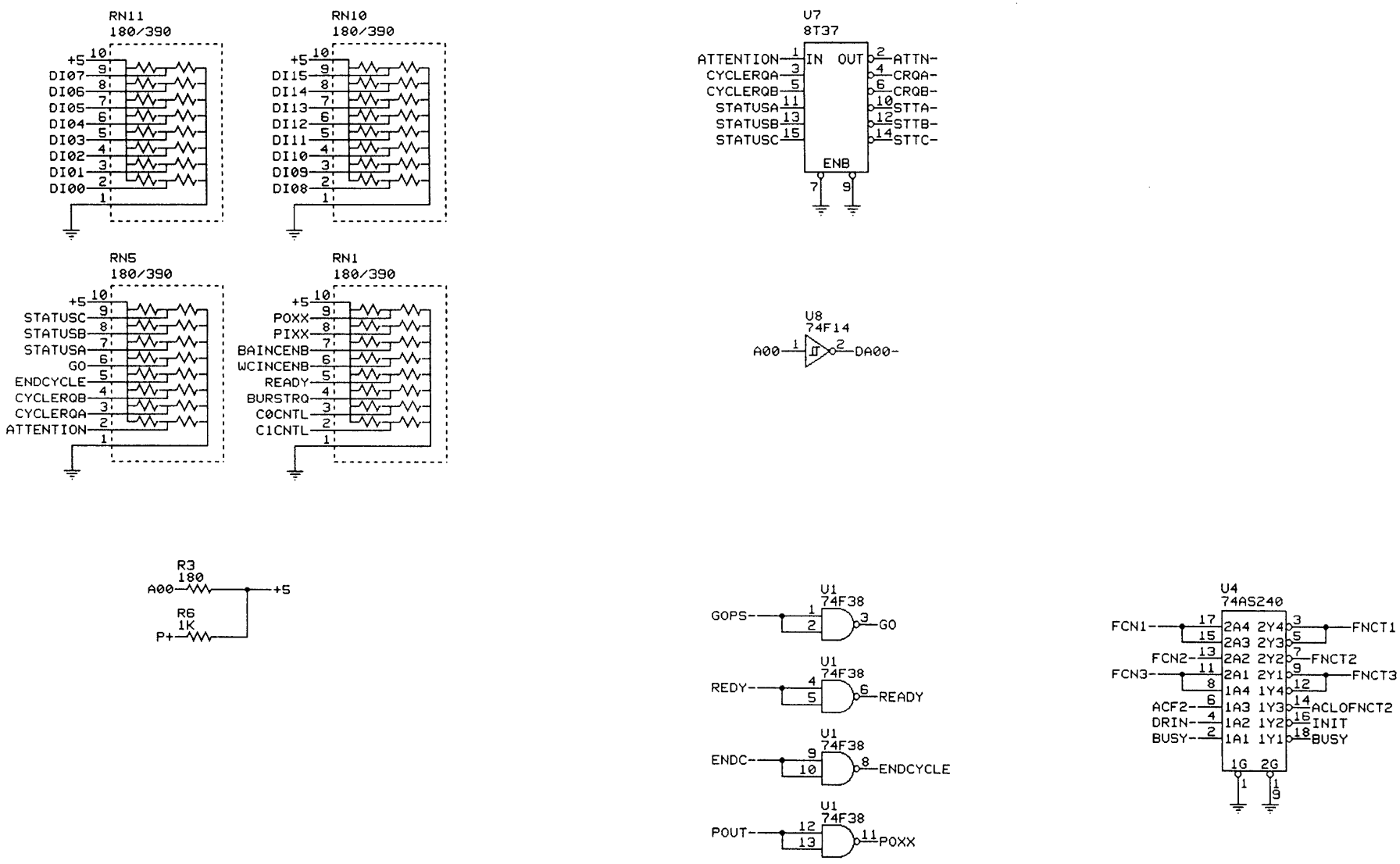
INPUT DATA PATH

IKON CORPORATION

PCI DR11 - 1/2 size

D | MODEL 10118 | REV etch

DATE: 12/05/96 13:42:52 | SHEET 07 OF 10



DR11 TERMS & CONTROL DR/RX

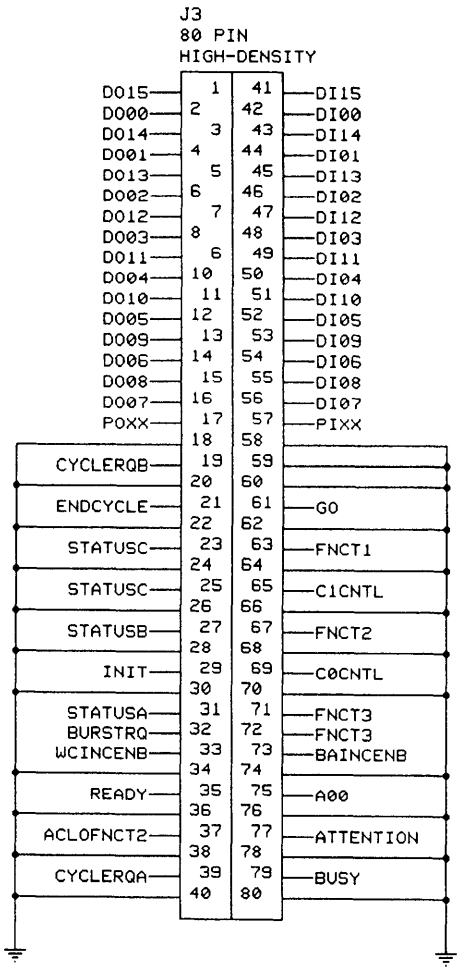
IKON CORPORATION

PCI DR11 - 1/2 size

D | MODEL 10118 | REV etch

DATE: 12/05/96 13:43:38 | SHEET 08 OF 10

NO 40-PIN HEADERS IN 10118

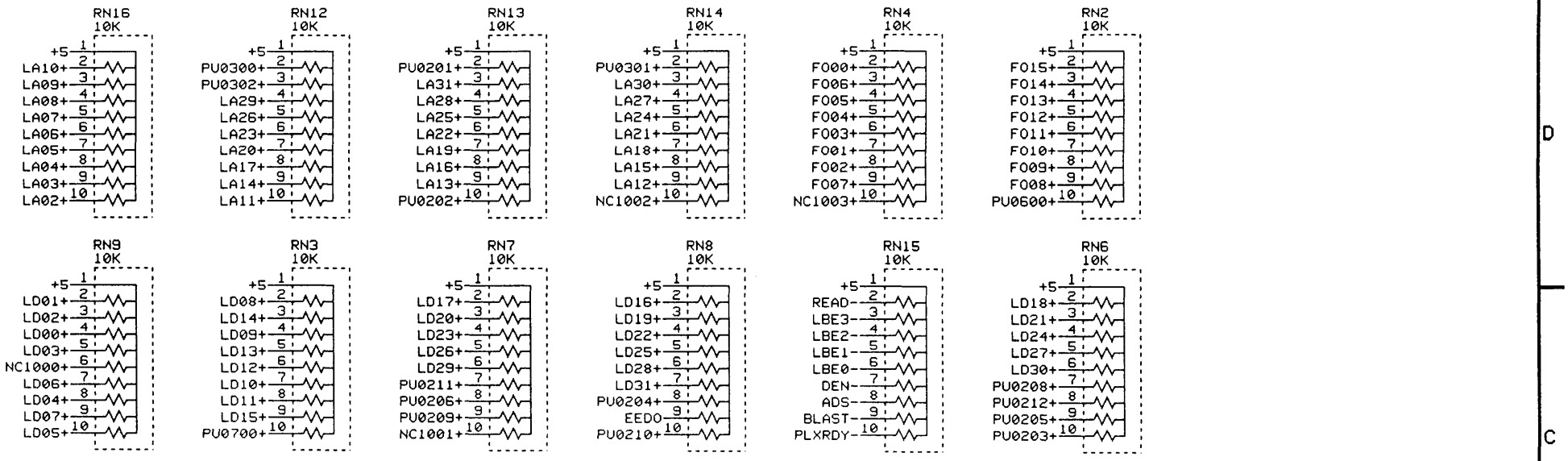


DR11 CONNECTORS

IKON CORPORATION

PCI DR11 - 1/2 size

D		MODEL 10118	REV etch
DATE: 12/05/96 13:44:25		SHEET 09 OF 10	



GROUND UNUSED INPUTS W/TRACE TO A HOLE

EACH PACKAGE GETS ONE 0.01 DECOUPLING CAP, EXCEPT FIFOS, WHICH GET TWO, AND FPGA AND PCI CHIP, WHICH GET FOUR EACH

DECOUPLERS SHOWN ARE ONE BULK ELECTROLYTIC AND 0.01uF DECOUPLERS FOR +5 AND +3.3 AT THE PCI CONNECTOR

THE 3.3V BUS WILL PROBABLY BE TAKEN OUT OF THE 5V PLANE, OR ON AN INTERNAL SIGNAL LAYER

PULLUP/DOWN & POWER & UNUSED

IKON CORPORATION

PCI DR11 - 1/2 size

D	10118	REV etch
DATE: 12/05/96 13:45:11	SHEET 10 OF 10	